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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/666,930 September 19, 2003 Filing Date First Named Inventor Farnworth et al. Group Art Unit 2812 Isaac Unknown **Examiner Name**

(use as many sheets as necessary)

te for form 1449A/PTO

of 5529US (02-0766.00/US) Sheet Attorney Docket Number

U.S. PATENT DOCUMENTS						
Initials *		Document Number	Publication Date	Name of Patentee or Applicant of	Day Caland Line ND - Outside	
	Cite No.1	Number - Kind Code ² (if known)	MM-DD-YYYY	Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
SDI.	Λ	US-6,251,488	06/26/01	Miller et al.	\	
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	111	US- 6,391,251	05/21/02	Keicher et al.		
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	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ³ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ [¢]	
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PTO/SB/08B(10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/666,930 Filing Date September 19, 2003 First Named Inventor Farnworth et al. Group Art Unit 2812 **Examiner Name** Unknown 5529115 (02-0766 00/115) Attorney Docket Number

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OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS						
Examiner Cite		-	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publicity and/or country where published.		T²	
SDI	U.S. Patent Application Publication 2002/0171177 A1 to Kritchman et al., dated November 21, 2002		U.S. Patent Application Publication 2002/0171177 A1 to Kritchman et al., dated November 21, 2002			
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			Webpage, Objet Prototyping the Future, "Objet FullCure700 Series", 1 page			
			Webpage, Objet Prototyping the Future, "How it Works", 2 pages			
			U.S. Patent Application No. 09/512,203, filed February 24, 2000, entitled "Tape Stiffener, Semiconductor Device Component Assemblies Including Same, and Stereolithographic Methods for Fabricating Same", inventor Ford B. Grigg		П	
	\prod		U.S. Patent Application No. 10/666,742, filed September 19, 2003, entitled "Support Structure for Thinning Semiconductor Substrates and Thinning Methods Employing the Support Structure", inventor Wood et al.			
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Examiner Signature	Hanetta Laac	Date Considered	3/4/05

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Sheet 1 of 1 Form PTO-1449 Docket Number (Optional) **Application Number** 5529US (02-0766.00/US) N t Yet Assign d **INFORMATION DISCLOSURE CITATION** 10/466,930 Applicant Farnworth et al. IN AN APPLICATION Group Art Unit Unknown (Use several sheets if necessary) Filing Date September 19, 2003 **U.S. PATENT DOCUMENTS** EXAMINER INITIAL DOCUMENT DATE NAME FILING DATE IF APPROPRIATE NUMBER CLASS SUBCLASS SVI 3,976,288 08/24/1976 Cuomo, Jr. 4,925,515 05/15/1990 Yoshimura et al. 5,460,703 10/24/1995 Nulman et al. 5,705,016 01/06/1998 Senoo et al. 6,039,830 03/21/2000 Park et al. 6,150,240 11/21/2000 Lee et al. 6,245,646 B1 06/12/2001 Roberts 6,344,402 B1 02/05/2002 Sekiya 6,498,074 B2 12/24/2002 Siniaguine et al. FOREIGN PATENT DOCUMENTS DOCUMENT DATE COUNTRY NUMBER CLASS SUBCLASS NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) SNI U.S. Patent Application Serial No. 10/094,161, filed March 6, 2002, for "Encapsulated Semiconductor Components and Methods of Fabrication." **EXAMINER** DATE CONSIDERED EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not consid red. Include copy of this form with n xt communication to the applicant.